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|  | **DEPARTMENT OF COMPUTER ENGINEERING** |

**Experiment No. 08**

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| Semester | S.E-Semester III – Computer Engineering |
| Subject | Digital Logic and Computer Architecture |
| Subject Professor In-charge | Prof. Avinash Shrivas |
| Assisting Teachers | Prof. Avinash Shrivas |

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| Student Name – Deep Salunkhe |
| Roll Number – 21102A0014 |
| Division and Batch – Division A, Batch 1 |
| Date of Implementation – 21/09/2022 |
| Experiment Title: To implement Decoder and Encoder |
| **Theory:**  Decoder  In digital electronics, a binary decoder is a combinational logic circuit that converts binary information from the n coded inputs to a maximum of 2ⁿ unique outputs  Encoder  An encoder in digital electronics is a one-hot to binary converter. That is, if there are 2ⁿ input lines, and at most only one of them will ever be high, the binary code of this 'hot' line is produced on the n-bit output lines. A binary encoder is the dual of a binary decoder |
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| **Implementation**  Decoder    Encoder |
| Conclusion |